

Figure 1 (Prior Art)

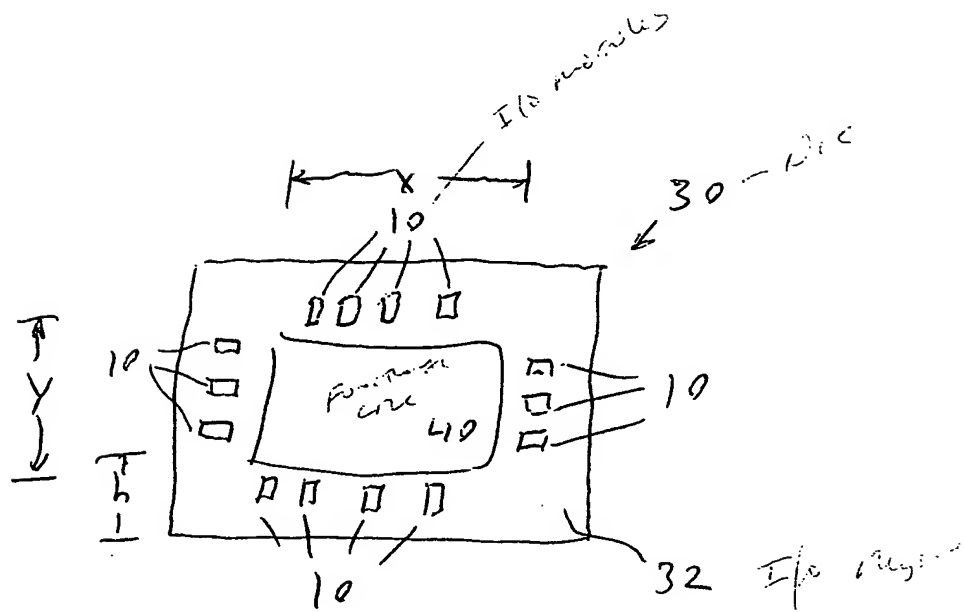


Figure 2 (Prior Art)

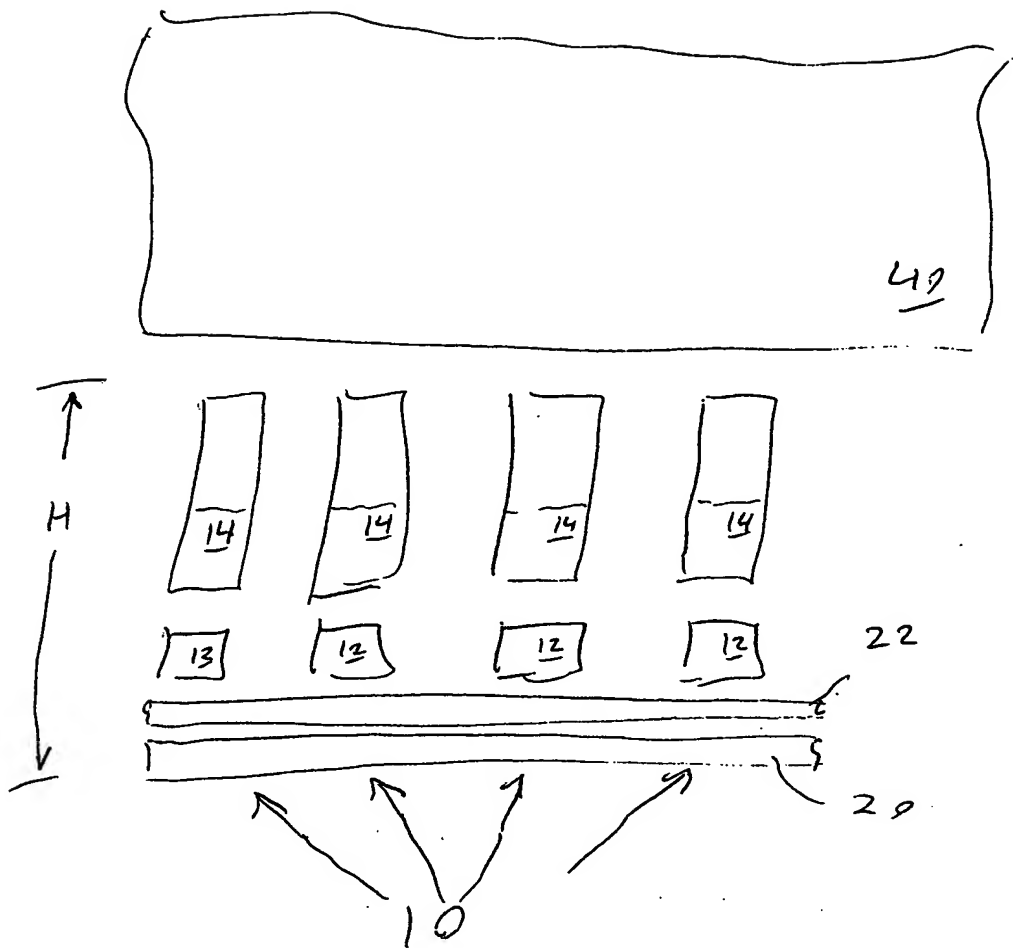
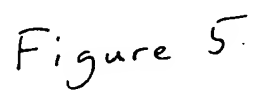
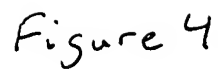


Figure 3 (prior Art)



Core area,mm2	mm /side	IO height, um	IO width, um	Area	max IO	wasted area	% of core
10	3.162278	H = 188	75	12.51941	169	1.67	13%
		Z = 120	108	11.57549	117		
20	4.472136	H = 188	75	23.50442	239	2.33	10%
		Z = 120	108	22.20423	166		
30	5.477226	H = 188	75	34.26025	292	2.83	8%
		Z = 120	108	32.68667	203		
40	6.324555	H = 188	75	44.89744	337	3.26	7%
		Z = 120	108	43.09339	234		
50	7.071068	H = 188	75	55.45882	377	3.63	7%
		Z = 120	108	53.45171	262		
60	7.745967	H = 188	75	65.96634	413	3.97	6%
		Z = 120	108	63.77566	287		

Figure 6

BEST AVAILABLE COPY

100
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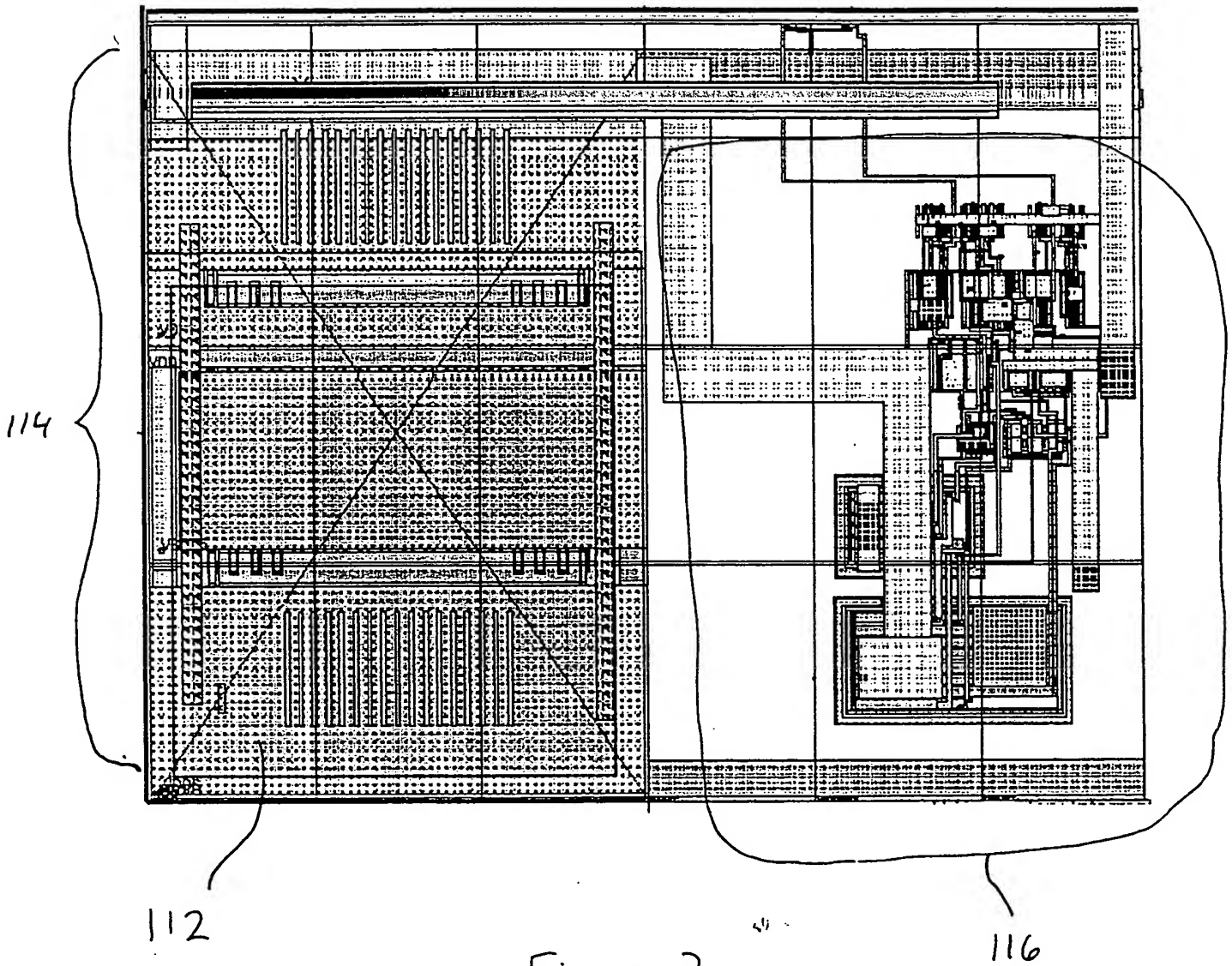


Figure 7

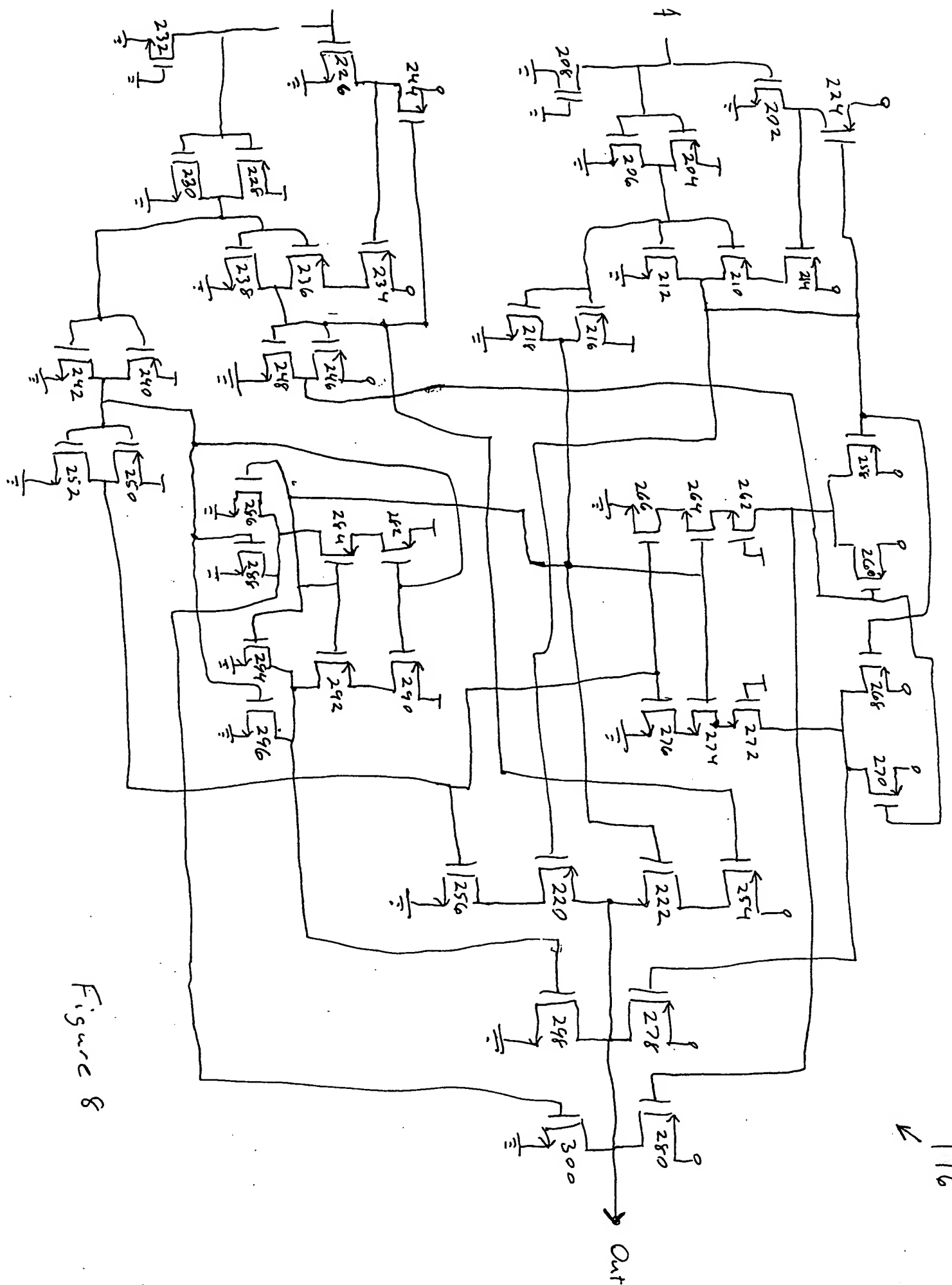


Figure 8